

FIG. 1A

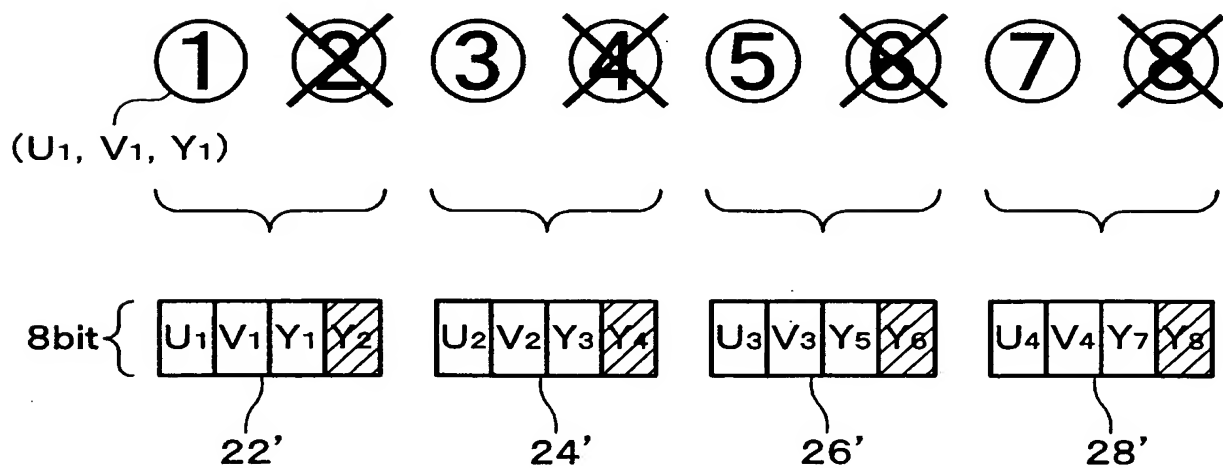


FIG. 1B

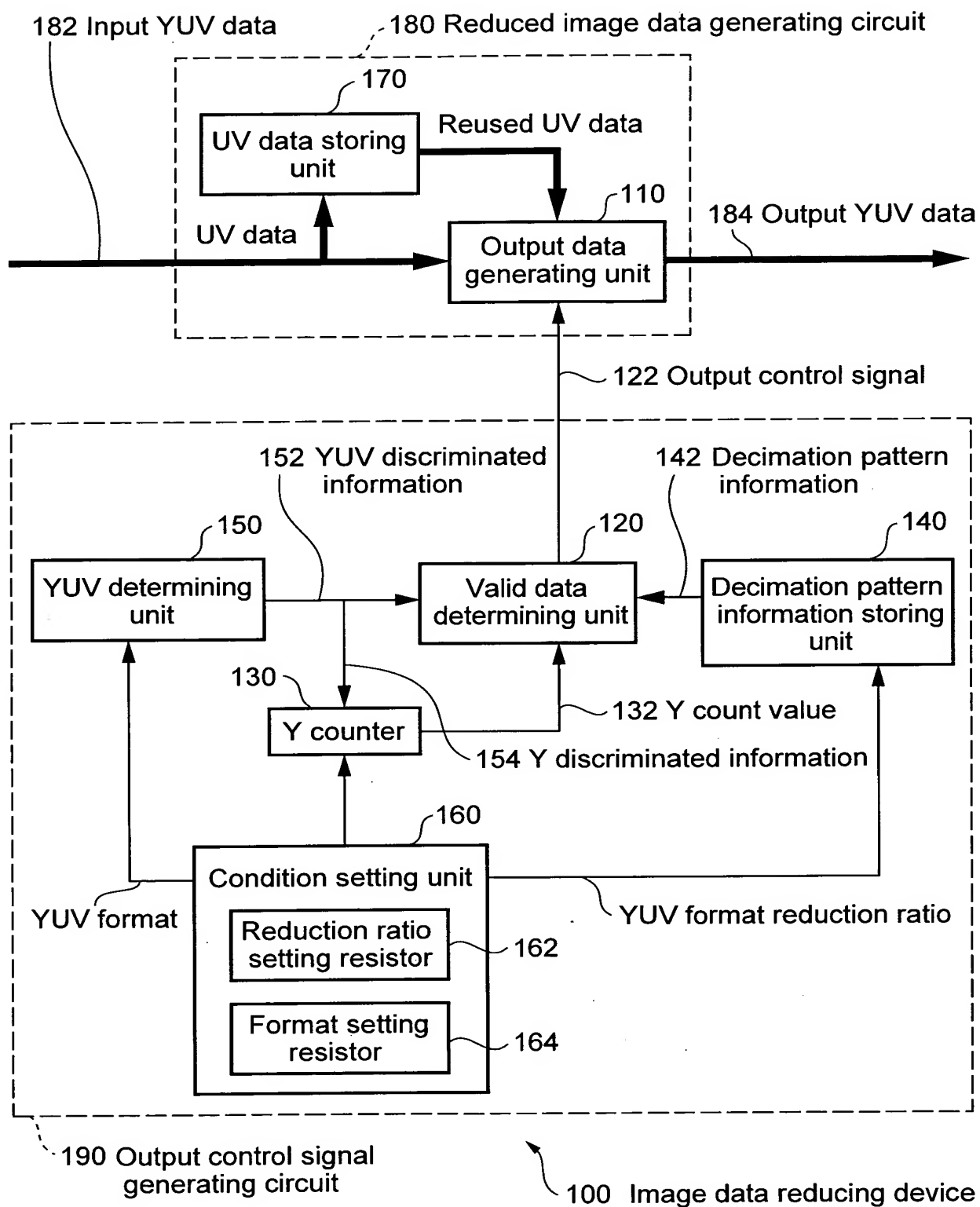


FIG. 2

YUV4:2:2 1/2 reduction

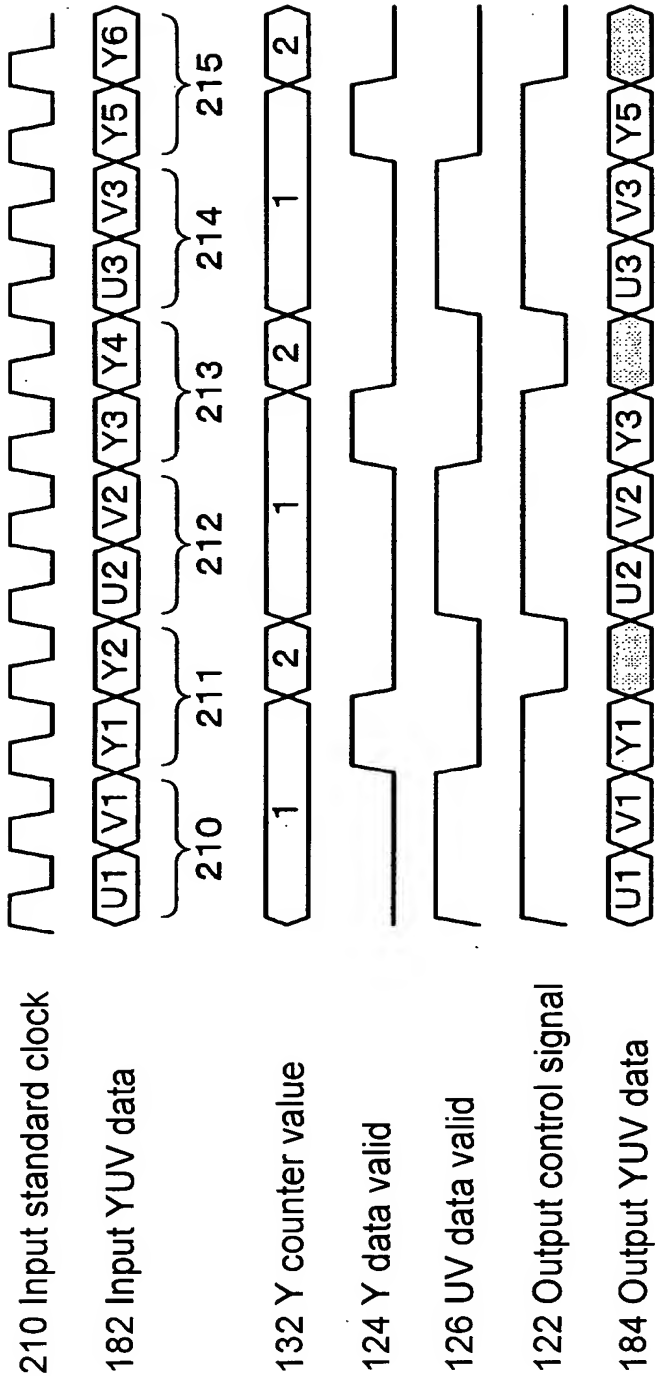


FIG. 3

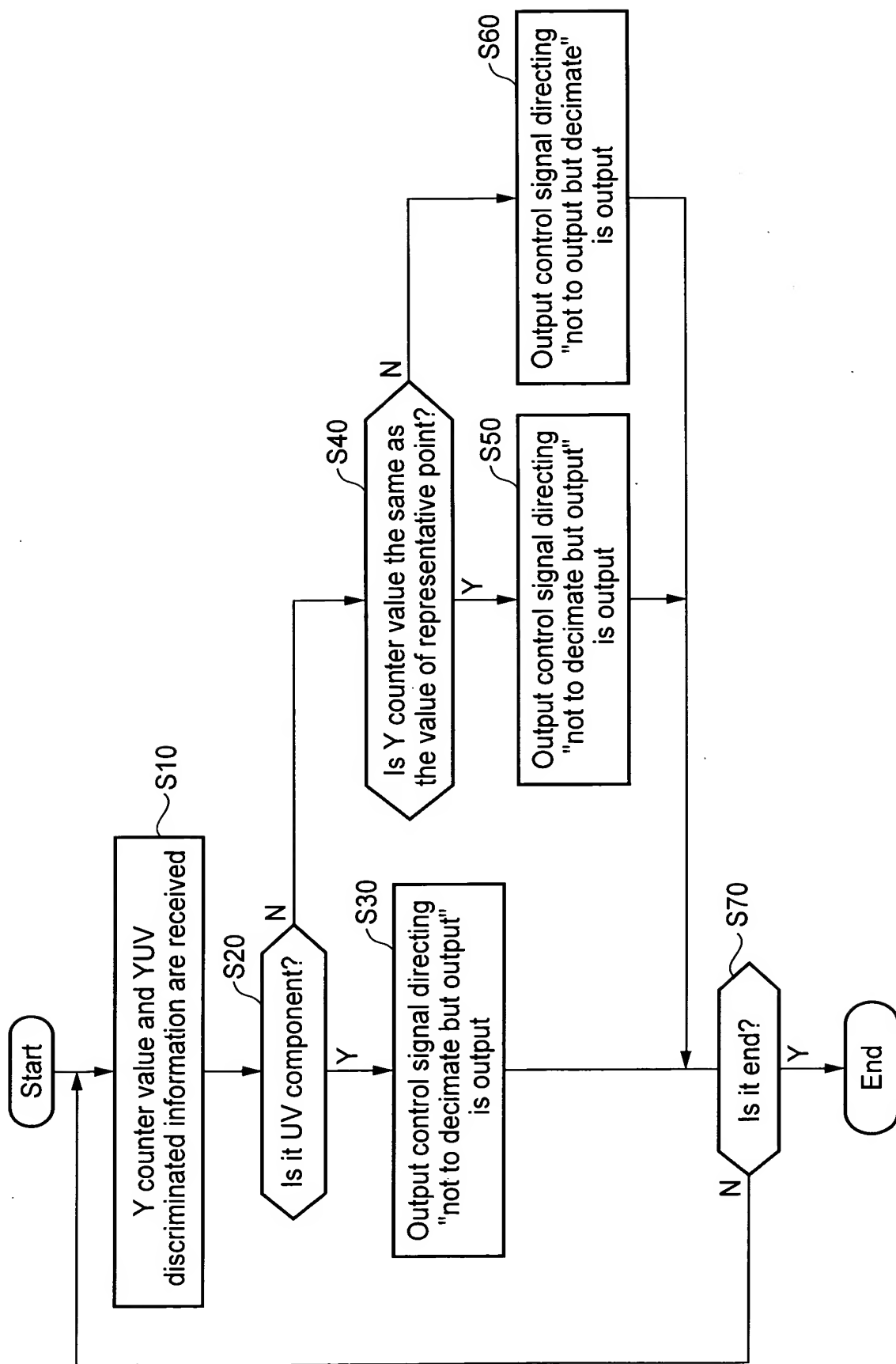


FIG. 4

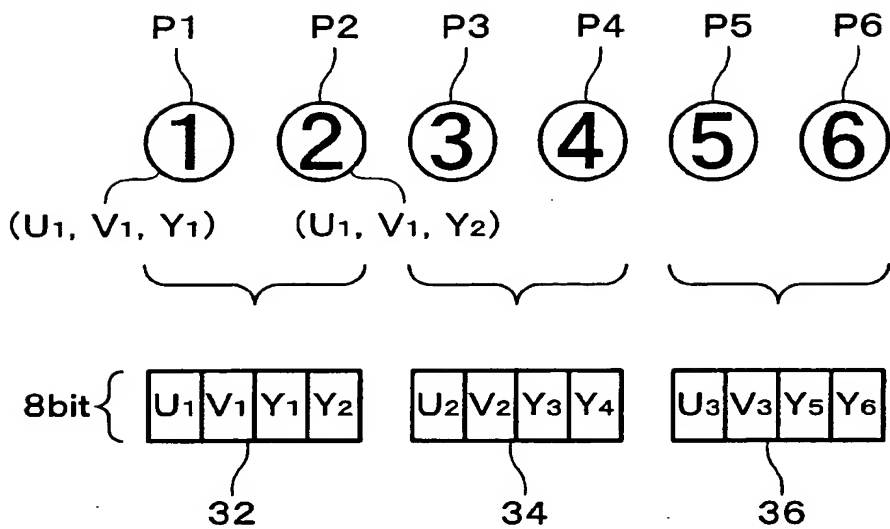


FIG. 5A

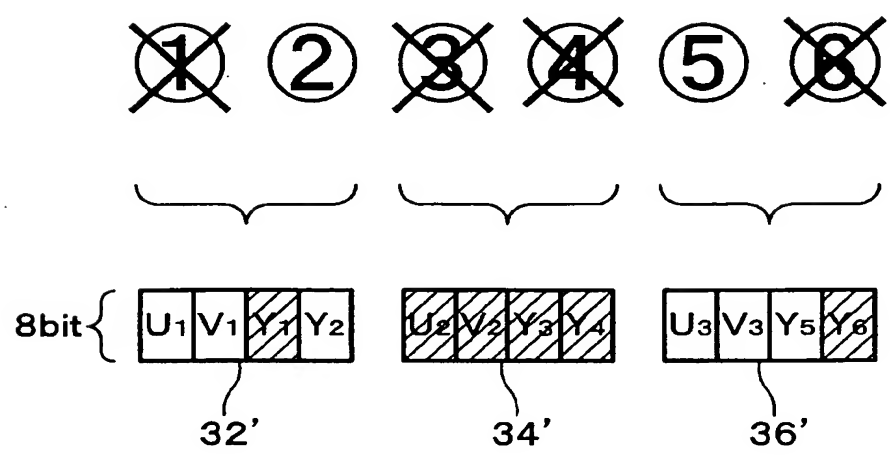


FIG. 5B

YUV4:2:2 1/3 reduction

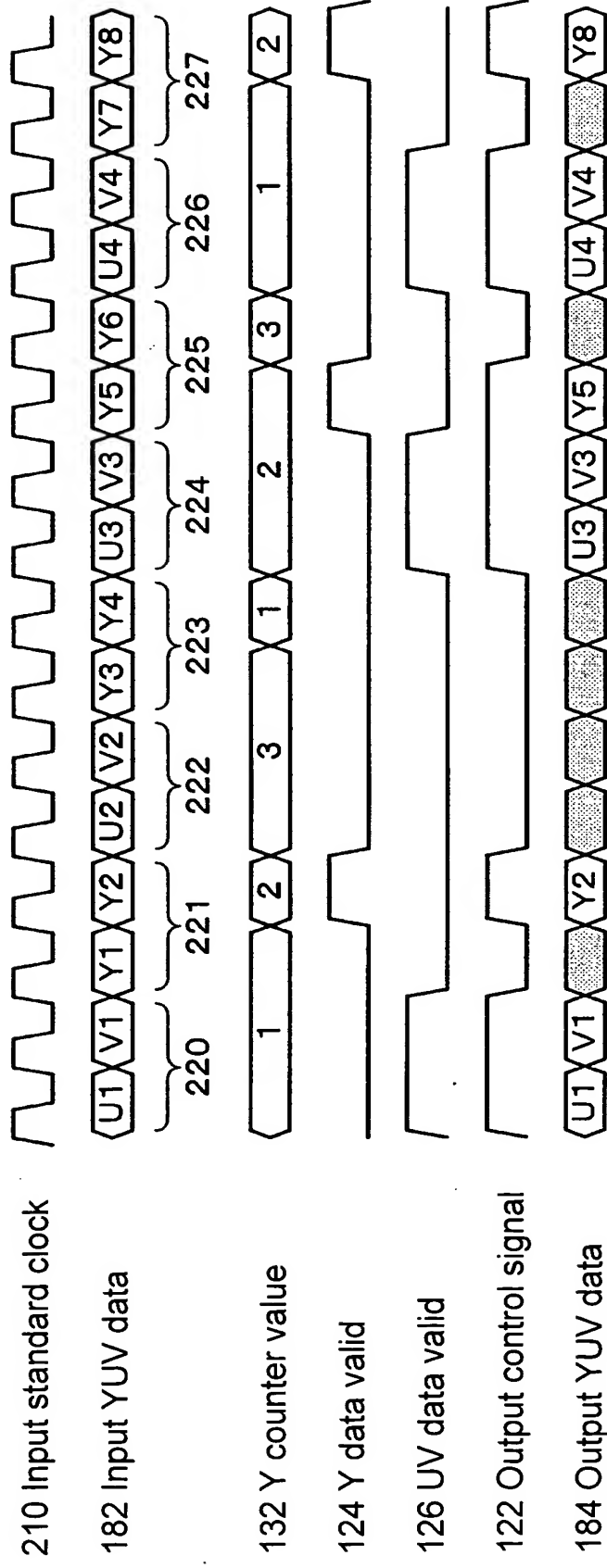


FIG. 6

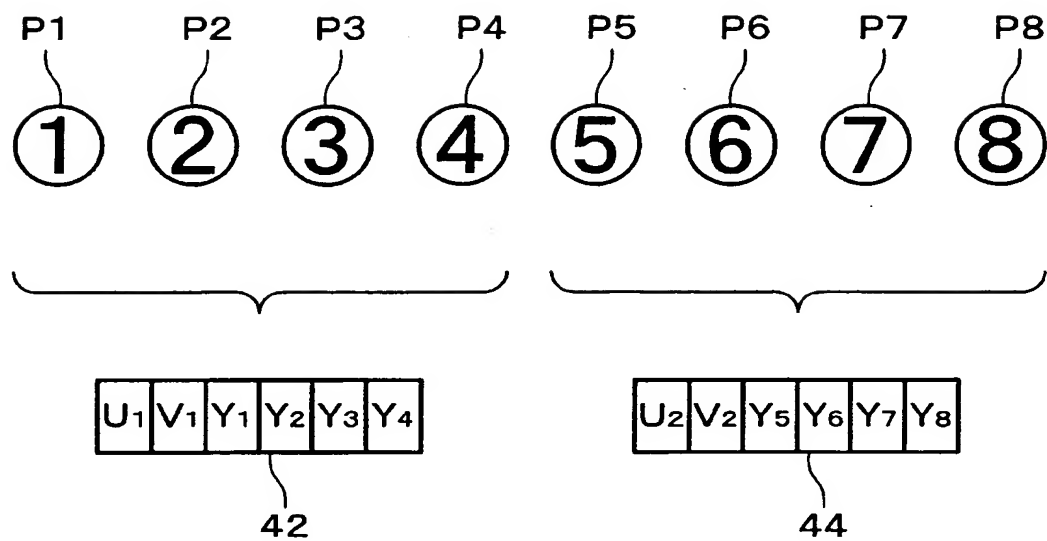


FIG. 7A

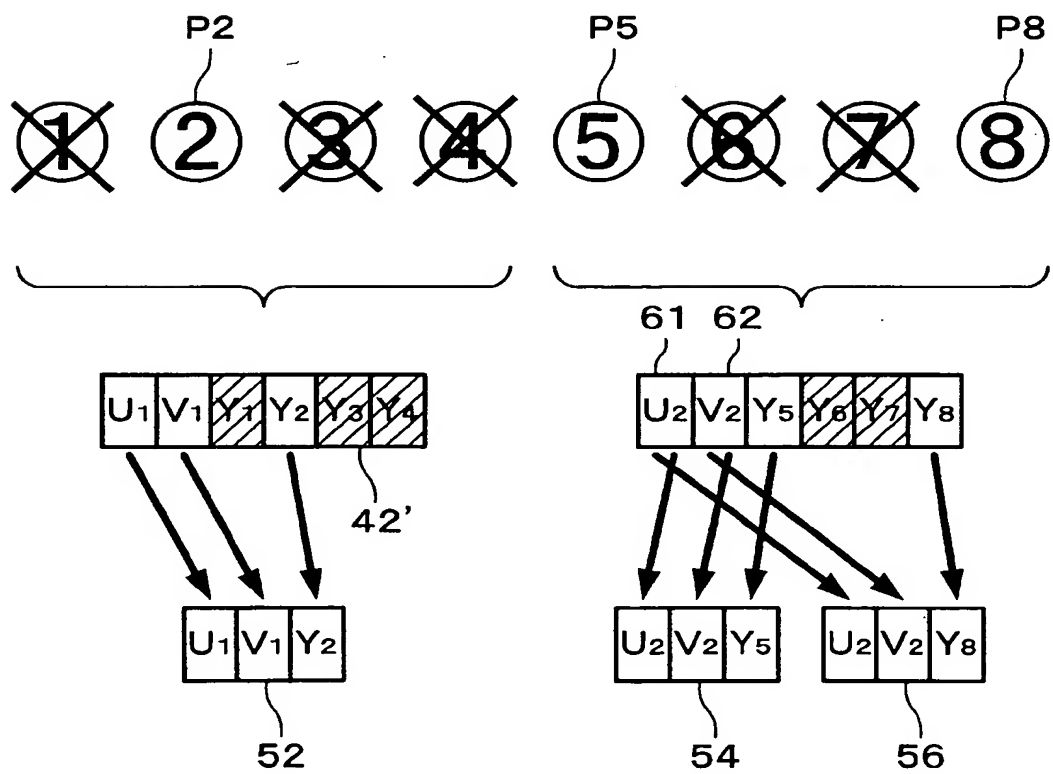


FIG. 7B

210 Input standard clock

182 Input YUV data

132 Y counter value

124 Y data valid

126 UV data valid

122 Output control signal

184 Output YUV data

P2

80

70 Copy

YUV format	Reduction ratio	Condition for outputting UV data
YUV4:4:4	All Case	When Y data is output
YUV4:2:2	1/2	Invariably output
	1/3	When Y counter is 1 or 2
	1/4	When Y counter is 1
	1/5	When Y counter is 2 or 3
	1/6	When Y counter is 3
	1/7	When Y counter is 3 or 4
	1/8	When Y counter is 3
YUV4:1:1	1/2	Invariably output
	1/3	Invariably output
	1/4	Invariably output
	1/5	When Y counter is not 4
	1/6	When Y counter is not 5
	1/7	When Y counter is 1, 2, 3, or 4
	1/8	Y counter is 1

FIG. 9

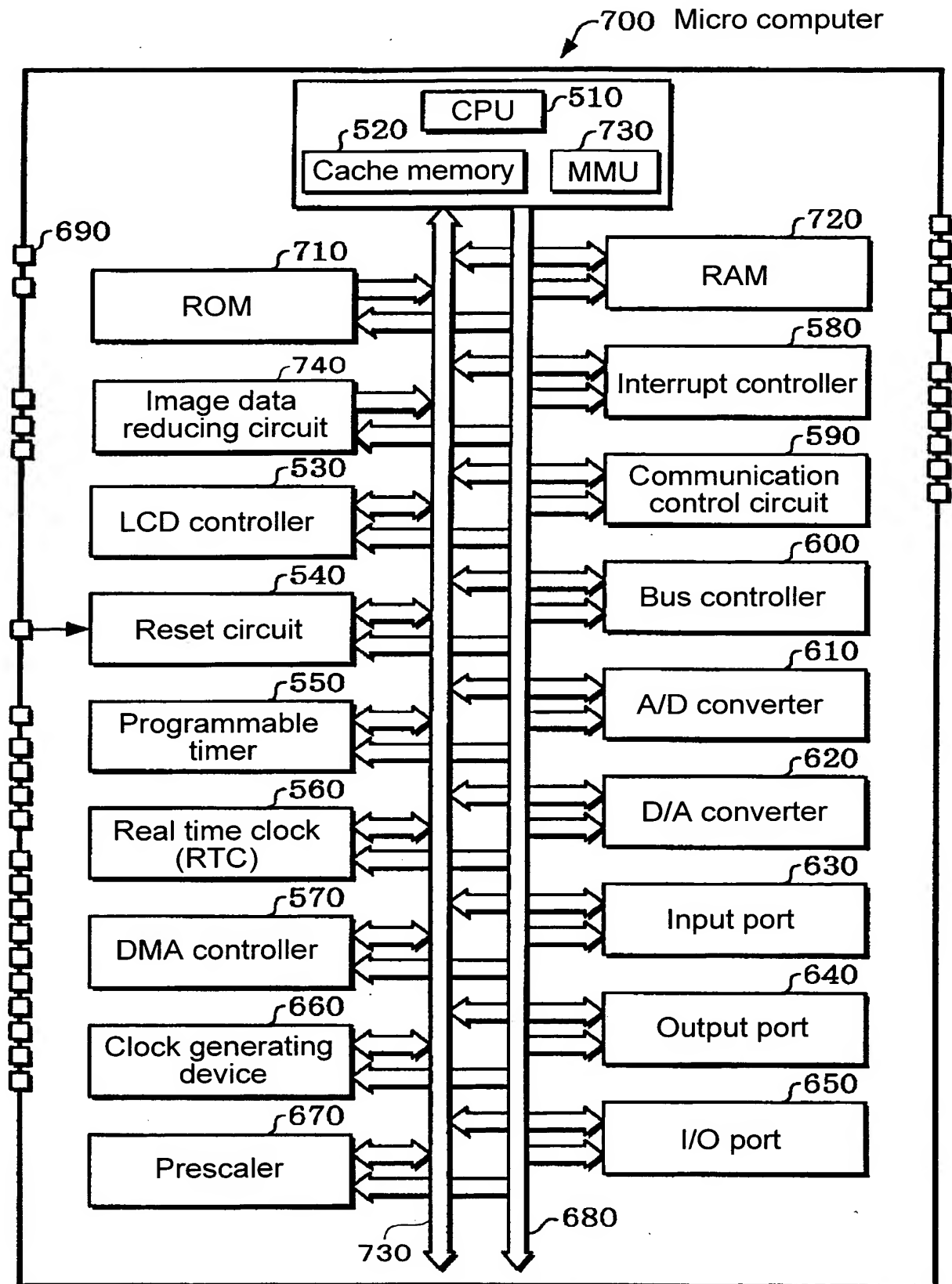


FIG. 10

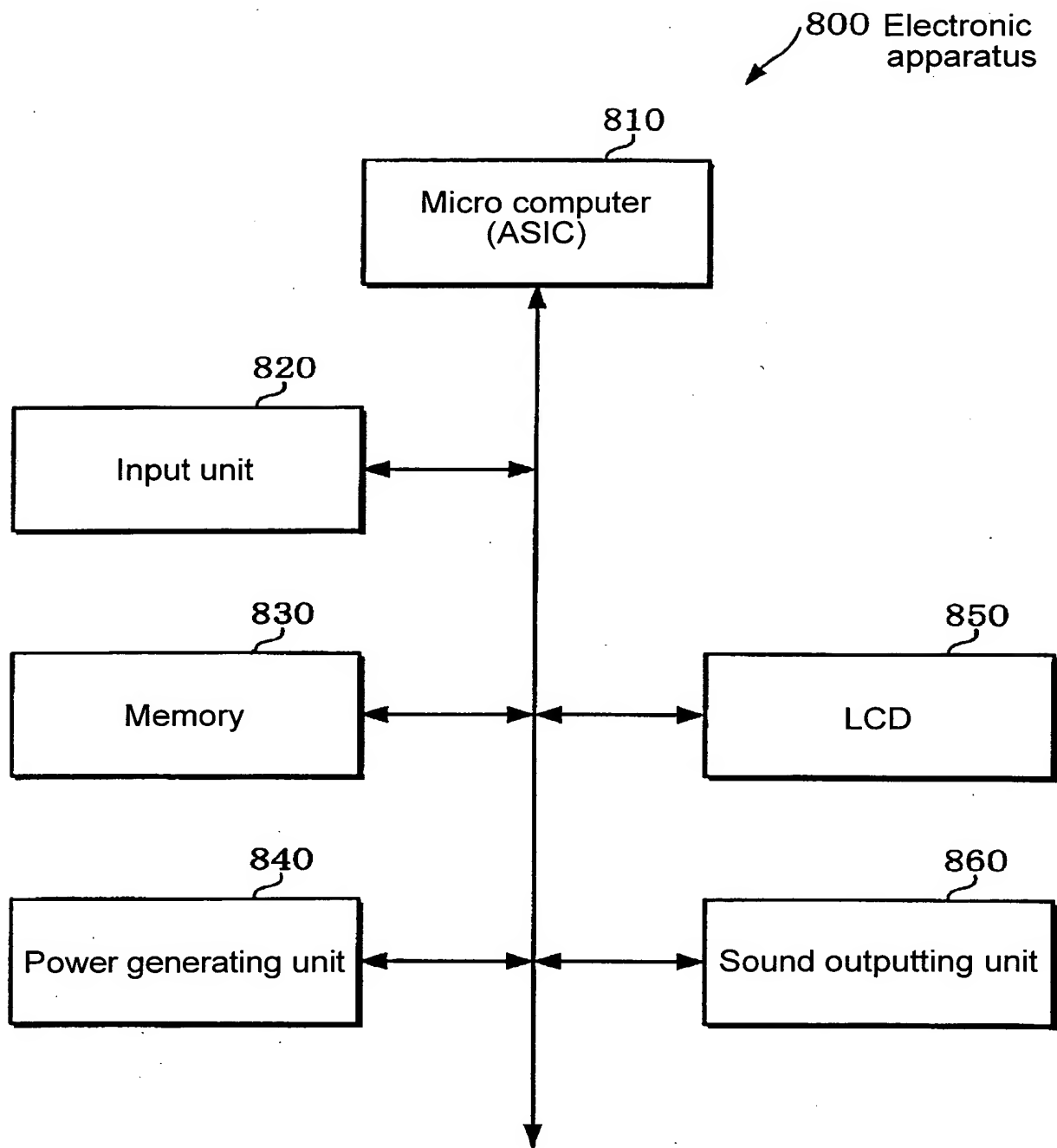


FIG. 11

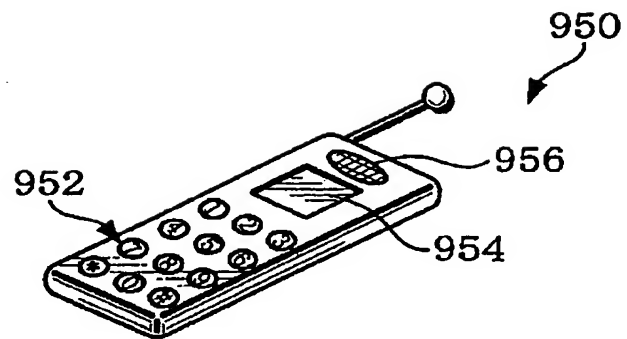


FIG. 12A

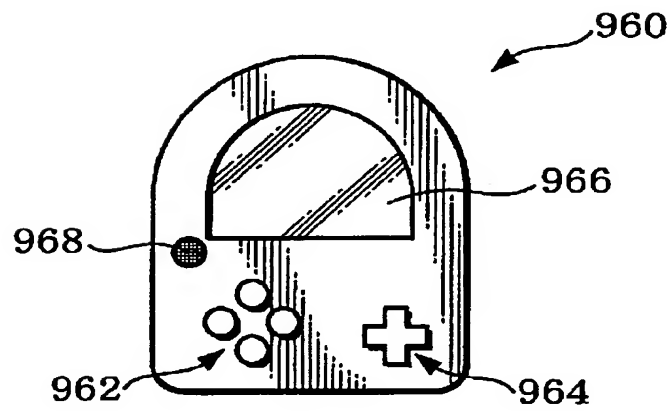


FIG. 12B

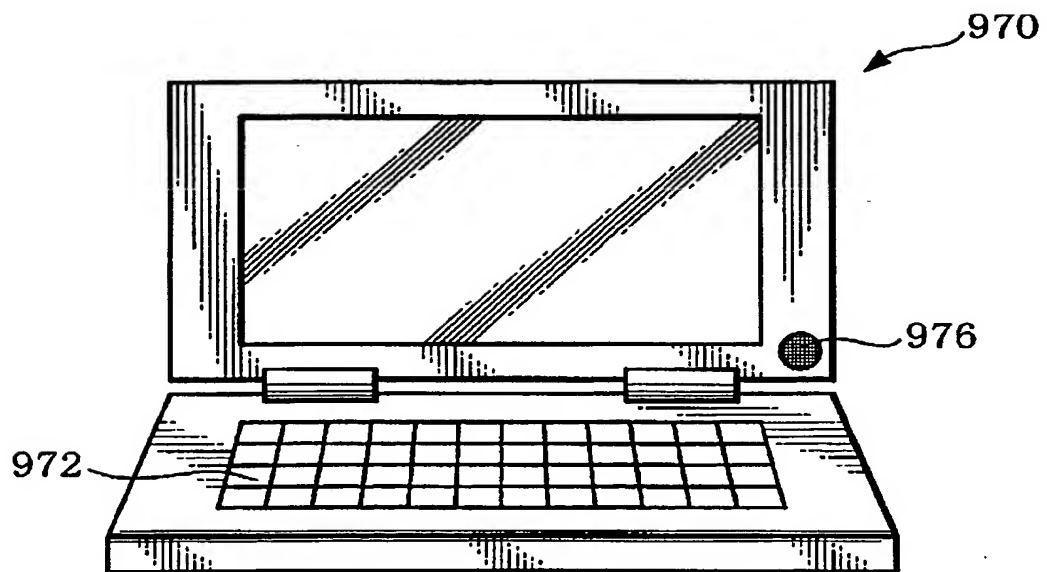


FIG. 12C